

# High Performance Analog And Digital Ics (Scas-02), Cas/Ssc Workshop

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**radio electronics magazine 02 february 1982** - Feb 01, 1982 Radio Electronics Magazine 02 February 1982. high-speed analog circ hard-to-read analog meters-or even digital meters that have seen

**digital-to- analog converter - wikipedia, the** - Digital-to-analog conversion can The audio DAC is a low speed high resolution type while the video DAC is a high speed 5 DAC performance; 6 DAC figures of

**high- performance interfaces using analog-** - May 10, 2010 The Ishikuro Lab is studying the design of interfaces for high-performance sensing. Such interfaces can rapidly exchange a large amount of information with

**ieee xplore** - 1997 2nd IEEE-CAS Region 8 Workshop on ; Analog and Mixed IC Design, Application Accelerators in High Performance Broadband Analog and Digital

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**ku leuven esat-micas - publications of prof** - "Synthesis of Integrated Passive Components for High-Frequency RF ICs Based on based analog-to-digital CAS/SSC chapter Workshop on

**ieee xplore abstract - high performance analog and** - An high performance phase-locked-loop (PLL) design method is discussed. In this context high performance means a high order PLL with efficient noise reduction and

**epa publications bibliography quarterly abstract** - &EPA United States Environmental Protection Agency Office of Research and Development Washington.

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**mixed-signal integrated circuit - wikipedia, the** - mixed-signal ICs are usually designed for a very specific purpose and their design requires a high digital performance analog and mixed-signal

**high- performance [delta sigma] analog-to-** - Title: High-performance [delta sigma] analog-to-digital conversion: Author: Tsang, Robin Matthew, 1979-Abstract: This dissertation is about a new [delta sigma] analog

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**outsource trade group > resources > commodity** - CAS# 601-89-8: 5 MHz Sampling Analog to Digital converter: USML XI(c) ECCN 9A991.d. 5/10/2011: High Performance L81 Series Low Noise Amplifiers L81 Series

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